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The Effect of Power MOSFET Materials on their Switching Performance

Asmaa S. Ibrahim¹ , Sanaa A. Kamh¹ , F. A. S. Soliman² and Doaa H. Hanafy1,*

¹Electronic Research Lab. (E.R.L.), Physics Department, Faculty of Women for Arts, Science, and Education, Ain-Shams University, Cairo, Egypt. ²Electronics Engineering Department, Nuclear Materials Authority, Ministry of Electricity and Renewable Energy, Cairo, Egypt.

Abstract

Power semiconductor devices have a great impact on the modern society electronic system applications. The study aims to improve the switching performance of power MOSFET devices based on materials with different bandgap structures and technologies. In this concern, the static electrical characteristics of three power MOSFET devices made of Silicon (Si-MTP20N15E; $Eg = 1.12$ eV), Silicon Carbide (SiC-TW107N65C; $Eg = 3.25$ eV) and Gallium Nitride (GaN-TPH3208PS-ND; $Eg = 3.44$ eV), operating at the enhancement mode were tested. Moreover, the design and implementation of their switching circuits were investigated at frequency of 1.0 kHz. In addition, the influence of their materials on the switching times was studied. From which, it is noted that, GaN MOSFET has the fastest switching ON/OFF times (0.3µs /0.34µs), rather than SiC and Si. Where, their switching ON/OFF times were reported to be (0.37µs /0 .52µs and 0.76µs /0.8µs), respectively. These findings underscore the potential of wide bandgap materials in enhancing the performance of power electronic devices.

Keywords: Switching times, power devices, MOSFET materials, Silicon, Silicon Carbide and Gallium Nitride.

1.0. Introduction

The expansion of the renewable energy and electronic sectors increases the need for high power density and high switching performance of power devices in the applications at different frequencies. However, increasing switching frequency results in higher switching losses in power transistors, which may reduce the efficiency of power systems [1, 2].

Power MOSFET devices having different materials and energy band gap such as Si (Eg=1.12eV), SiC (Eg =3.25 eV) and GaN (Eg = 3.44 eV) have been subjected to extensive research [3]. They are characterized by high operating temperatures (Si: 150 °C, SiC: 175 °C

Email: doaa.hassan@women.asu.edu.eg

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^{*}corresponding author: Doaa H. Hanafy, Electronic Research Lab. (E.R.L.), Physics Department, Faculty of Women for Arts, Science, and Education, Ain-Shams University, Cairo, Egypt.

and GaN: 150°∁) and switching speeds, in addition they are considered to be the next evolutionary step for future in modern power applications [4,5].

In the present paper, the effect of the power MOSFET materials (Si, SiC and GaN) on their dynamic switching charecteristics are investigated at frequency of 1.0 kHz. In this concern, the switching circuits based on the tested power devices are designed and implemented for testing their dynamic switching characteristics.

2.0. Methodology and Circuit-setup

In this work, power MOSFET device with different materials (Si; MTP20N15E, SiC; TW107N65C and GaN; TPH3208PS-ND) operating at the enhancement mode are selected and tested, where their static electrical characteristics (I–V) are plotted using Tektronix curve tracer type 370A. From which, the ON-resistance (R_{ON}) , generated power (P_{gen}) , dissipated power $(P_{dis.})$, and forward transconductance (g_{fs}) , are calculated as follows [6-9].

The resistive parameter is described as R_{ON} and is given as:

 $R_{ON} = \frac{\Delta V_{DS}}{\Delta I_{D}}$ ∆I^D ………………………………………………………..(1)

The ON-resistance is equivalent to different components which are;

RON = Rsource + Rch + R^A + R^J + R^D + Rsub…………………….………….…. (2)

Where:

Rsource: MOSFET source resistance,

Rch : MOSFET channel resistance,

RA : MOSFET accumulation resistance,

RJ : JFET region resistance,

RD : MOSFET drift region resistance and

Rsub. : MOSFET substrate resistance.

Output power generation of MOSFET device could be calculated using the following equation:

Pgen. ≈ IDVDS……………………………………………………………….….…..(3)

As well, the dissipated power of MOSFET device could be calculated using the following equation:

Pdis. = Conduction Loss + Switching Losses……...……………………….…..(4) Where:

Conduction Loss = I^D ²RON…………………………..……….……………….…..(5)

As well, switching losses are composed of the total gate charge loss, C_{OSS} loss and rise and fall time loss.

Switching loss = Total Gate Charge loss + C_{out} loss + rise and fall time loss.....(6)

Where:

Cout : the output capacitance of MOSFET device.

Moreover, the forward transconductance of MOSFET device could be calculated using the following equation;

 $g_{fs} = \frac{\Delta I_D}{\Delta V_G}$ $\Delta {\rm V_{GS}}$ |VDS ….………………………….………………….…………………… (7)

Where:

V_{DS}: drain-source voltage, ∆ID: drain current change and ∆VGS: gate-source voltage change.

2.1. Switching Circuit-setup

The investigated switching circuit-setup is shown in Fig. (1), it consists of power MOSFET device, external gate resistance $(R_{\text{gex.}})$, as well load resistance (R_L) .

Fig.(1): Tested switching circuit.

2.2. Dynamic Switching Transient Times

Theoritically, the switching input/ output voltage waveforms combined with its dynamic switching transient times (delay/rise times; t_d/t_r , as well storge/fall times; t_s/t_f) are shown in Fig. (2). For insuring the best switching performance for the power MOSFET devices, the optimum circuit elements are selected carefully [10-12].

Fig.(2): Input/Output voltage waveforms.

The time required for a power device to switch to its conduction state (ON-time), is given by the following equation[11]:

tON = t^d + tr…………………………………………………………….….…. (8)

Where: ton: ON-time, t_d: delay time and

 t_r : rise time.

From Fig. (2), it is shown that the time needed from the transistor switch to start responding to a rising edge of the driving pulse (input) is called the delay time (t_d) . Furthermore, the rise time is the finite time needed for the output voltage (V_{out}) to fall 90% to 10% of its maximum value.

While the turn OFF-time (t_{OFF}) , is the time required for a MOSFET to switch to its disconduction state is given by the following equation [10]:

tOFF = t^s + tf……………………………………………………………….….…. (9)

Where: t_{OFF}:OFF-time, ts: storage time and t_f : fall time.

The storage time (t_s) is the time needed for the switch to start responding to a falling edge of the input voltage (V_{in}) . While, the fall time (t_f) is the finite time needed for the output voltage (V_{out}) to rise from 10% to 90% of its maximum value.

The dynamic switching performance of the power switches are affected by several factors which are the operating conditions [input voltage (V_{in}) and the circuit elements such as [external] gate resistance $(R_{\text{gex.}})$ as well, load resistance (R_L)].

2.2.1. Effect of the Input Voltage and the External Gate Resistance

The effect of the input voltage and external gate resistance variations on the dynamic switching transient times of the power devices are studied. The dynamic switching transient times of the tested MOSFET devices could be calculated using the following equations [8,13].

t^d ≅ RGCin ln (1 1− Vgp VGS)……………………………………………………...……. (10)

Where:

R_G: the equivalent gate resistor ($R_G = R_{gi} + R_{gex}$), R_{gi} : the internal gate resistance and R_{gex} : the external gate resistance,

 C_{in} : the equivalent input capacitance ($C_{\text{in}} = C_{\text{GS}} + C_{\text{GD}}$) as seen by the gate input circuit, where; C_{GS} : the internal gate-source capacitance and C_{GD} : the internal reverse transfer gate-drain capacitance (miller capacitance) where C_{GD} \geq C_{GS} . Using Miller theorem [14], $C_{in} = C_{GS} + (1 +$ g_mR_L) C_{GD}], where; g_m : the product of the slope of I_D and V_{GS} with operating dc point (Q),

 V_{gp} : gate miller plateau voltage and

VGS: gate-source voltage.

Considering
$$
t_r \approx R_G C_{gd} \left(\frac{v_{DS}}{v_{GS} - v_{gp}} \right)
$$
.................(11)
\nMoreover $t_s \approx$
\n $R_G C_{iss} \ln \left(\frac{v_{GS}}{v_{gp}} \right)$(12)
\nAs well $t_f \approx$
\n $R_G C_{gd} \left(\frac{v_{DS}}{v_{gp}} \right)$(13)

2.2.2. Effect of the Load Resistance

For the tested power MOSFET switches, the dynamic switching transient times dependency on the load resistance are studied. Concerning the fall time (t_f) dependency could be calculated using by the following equation [15-18]:

t^f = −RLCLIn(Vout 10%/Vout 90%)…………………………………………….….. (14)

Where:

RL: load resistance and C_L: load capacitance.

3.0. Results and Discussions

The main aim of this work is to investigate the influence of the tested power MOSFET materials for the (Si-MTP20N15E, SiC-TW107N65C and GaN-TPH3208PS-ND) on their static characteristics as well their dynamic switching characteristics.

3.1. Devices Characteristics

The output and transfer static characteristics ofthe tested power MOSFET devices are carried out and plotted at different V_{GS} values depending on their materials.

3.1.1. Static Output Characteristics

The device materials effect on their (I-V) static output characteristics plotted at different V_{GS} values are shown in Fig. (3). For Si-device (3.5 – 6.0 Volts), and SiC (6.5 – 9.5 Volts) as well GaN $(2.5 - 3.25$ Volts).

Fig. (3): (I-V) Characteristic curves of different MOSFET device materials, plotted at different V_{GS} values.

3.1.1.1 ON-Resistance of MOSFET Devices

 ON -resistance (R_{on}) represents the internally resistance element of MOSFET device which is responsable for power disspation. Figure (4) explores the influence of increasing V_{GS} on Ron of the tested MOSFET materials. From which, it is clearly shown that, for Si-MOSFET, the value of R_{ON} decreases from 9.09 Ω to 0.6 Ω, measured at V_{GS} value of 3.5 Volt up-to 4.5 Volt, respectively. Also, SiC MOSFET, the value of R_{ON} decreases from 10 Ω to 0.46 Ω , measured at VGS value of 6.5 Volt up-to 8.9 Volt.Whenever GaN MOSFET, the values of Ron decreases from 3.1 Ω to 0.31 Ω , measured at V_{GS} value of 2.5 Volt up-to 3.25 Volt, respectively. For higher values of V_{GS} a saturation conditions on R_{on} were observed.

Fig. (4): Effect of V_{GS} on R_{on} for different MOSFET device materials.

3.1.1.2 Output Power Generation of MOSFET Devices

The influence of MOSFET materials on their output power generated are investigated and plotted for different V_{GS} values as shown in Fig. (5). It is clearly shown that, generally the output power generated depends on both voltages V_{GS} and V_{DS} . The higher power generated reaches the value of 79.8 W at $V_{GS} = 6$ Volt and 40 W at $V_{GS} = 9.5$ Volt, while the values of 60 W at $V_{GS} = 3.25$ Volt, for Si and SiC as well GaN, respectively.

It is noted that, these variations in output power generated may be attributed to the difference in their threshold values, as shown in Table (1).

Fig. (5): (P-V) Characteristic curves of different MOSFET device materials, plotted at different V_{GS} values.

3.1.2. Transfer Characteristics of MOSFET Devices

The device materials influence on their transfer characteristics (I_D-V_{GS}) plotted at constant (V_{DS} = 6.0 Volt) are illustrated in Fig. (6). Their threshold voltage values are recorded to be 3.4, 6 and 2.1 Volt for Si, SiC and GaN MOSFET devices, respectively. After these values there is a remarked rapid increase in their drain current (I_D) values which reflected that, the devices are in conducting state. It is to be noted that, the tested GaN device has the lowest threshold value (2.1 Volt).

Fig. (6): Transfer characteristics for different MOSFET device materials.

In addition, the effect of the tested power device materials on their threshold voltage and the forward transconductance (g_{fs}) are summarized and tabulated in Table (1). It is observed that, GaN device has a higher conductivity (11.5 Ω^{-1}) which is attributed to its lower threshold voltage value (2.1 Volt).

Table (1): MOSFET device materials effect on their static electrical parameters.			
Electrical Parameters	Power MOSFET Devices		
		SiC -	GaN
V _{Th} , Volt	3.4	6.0	
g_{fs}, Ω^{-1}	3.08	114	115

Table (1): MOSFET device materials effect on their static electrical parameters.

3.2. Switching Characteristics

The switching characteristics of the tested MOSFET devices are experessed as their switching transient times (t_{ON}, t_{OFF}) .

The switching output voltage waveforms of different MOSFET materials, are affected by different factors. Namely; input voltage (V_{in}) and external gate resistance (R_{gex}) as well, load resistance (R_L) .

3.2.1. Effect of the Input Voltage

In this concern, Fig. (7) displays the cuptured input / output voltage waveforms (Y-axis: 10 V/div. and X-axis: 1.0 µs/div.) illustrating the switching behaviour of three different MOSFET materials under the influence of varying input voltage (V_{in}) from 9.0 volt up to 20 volt.

It is noted that, a Miller plateau is clearly observed in certain MOSFET device. Figure (7.a) highlights its presence in Si-MTP20N15E MOSFET waveform. This plateau arises due to the combined effects of input capacitance and the Miller capacitance.

Conversely, SiC and GaN exhibit lower Miller capacitance compared to conventional MOSFETs, leading to lower switching losses and faster switching speeds as shown in Fig. (7.b, 7.c). Notably, The cascode topology of employed in GaN manages the Miller effect during transients (dv/dt), as evident in Fig. (7.c).

Fig. (7): The switching output voltage waveforms of different MOSFET materials dependency on the varitions of input voltage values.

In addition, depending on the switching output voltage waveforms (Fig.7), the switching transient times(t_{ON} , t_{OFF})of the tested MOSFET device materials are illustrated in Fig. (8). It is clearly shown that, their ON-times decreases by increasing input voltage value. Where its value decreases from 5.08 to 0.76 for Si, 0.865 to 0.37 for SiC and 0.52 to 0.3 for GaN by increasing of input voltage from 9 Volt to 20 Volt. In addition, the OFF-time of switch decreasesby increasing the input voltage value. Where its value slightly decreases from 1.04 to 0.9 for Si, 0.65 to 0.52 for SiCand 0.355 to 0.34 for GaN.

Fig. (8): Switching transient times of different MOSFET materials (a) ON-time and (b) OFF-time, plotted at different input voltage values.

3.2.2. Effect of the External Gate Resistance

The influence of the external gate resistance (R_{gex}) variation ranging from 10.0 Ω up to 100.0 Ω on the switching output voltage waveforms for different MOSFET materials is shown in Fig. (9). From which, it is clearly shown that, the external gate resistance variation show a remarked effect on the switching output voltage waveforms for different MOSFET materials. It is observed that, GaN-MOSFET device has the lowest dependency on R_{gex} than the other tested materials.

waveformsfor different MOSFET materials.

In this concern, depending on the influence of the external gate resistance on the switching output voltage waveforms Fig. (9), the switching transient times (t_{ON}, t_{OFF}) of the tested MOSFET device materials are shown in Fig. (10). From which it is clearly shown that, their ONtimes are affected by chaning of R_{gex}. values. It is observed that, considering GaN switch its ON-

time shows a little increases (from 0.3 up to 0.4 μ s - duration time = 0.1 μ s) than SiC (from 0.37 up to 0.69 μ s -duration time = 0.32 μ s) and Si (from 0.76 upto 1.15 μ s - duration time = 0.39 μ s), respectively.

Moreover, for GaN switch its OFF-time shows a little increases (from 0.34 upto 0.344 µs -duration time $= 0.004$ us) than SiC (from 0.52 up to 0.6 us - duration time $= 0.08$ us) and Si (from 0.8 up to 0.9 μ s - duration time = 0.1 μ s), respectively.

(b) OFF-time, plotted at different external gate resistance values.

3.2.3. Effect of the Load Resistance

The dependency of output voltage waveforms for different MOSFETs materials on load resistance (R_L) variations from 116 Ω up to 1.0 k Ω is studied and plotted as shown in Fig. (11). The results clearly demonstrate a significant effect of R_L variation on the output waveforms for all materials. Notably, GaN MOSFETs exhibit the least dependence on changing R^L values compared to the other tested devices.

for different MOSFET materials.

Concerning the influence of the load resistance on the switching output voltage waveforms (Fig.11), the switching transient times (t_{ON}, t_{OFF}) of the tested MOSFET device materials are illustrated in Fig. (12). From which it is clearly shown that, their ON-times are slightly increases by increasing RLvalues. It is observed that, considering GaN switch its ON-time shows slightly increase (from 0.3 up to 0.32 μ s - duration time = 0.02 μ s) compared with SiC (from 0.37 up to 0.395 μ s - duration time = 0.025 μ s) and Si (from 0.76 up to 0.79 μ s - duration time = $0.03 \,\mu s$), respectively.

On the other hand, their OFF-time shows a remarkable increase with increasing the load resistance value which is consistent with (Eq. 13). Considering GaN switch its OFF-time shows the lowest increase (from 0.34 up to 1.36 μ s - duration time =1.02 μ s) followed by SiC (from 0.52 up to 1.64 μ s - duration time =1.12 μ s) and Si (from 0.8 up to 2.74 μ s - duration time = 2.06 μ s), respectively.

Fig. (12): Switching transient times of different MOSFET materials(a) ON-time and (b) OFF-time, plotted at different load resistance values.

The proposed switching circuit is designed after selecting the optimum suitable operating conditions and circuit element values (V_{GS} = 20 Volt, R_{gex}=10 Ω and R_L= 116 Ω) according to the obtained results as shown in Fig. (13).

Fig. (13): The proposed switching circuit.

Figure (14) compares the switching output voltage waveforms of the three tested MOSFET device materials. Notably, GaN MOSFETs exhibit superior performance in terms of both transient times and Miller plateau suppression over the other tested devices.

For comparison, Fig. (15) shows the effect of MOSFET device materials on the switching transient times. From which, it is observed that, for GaN MOSFET has the best switching transient times with ON-time of 0.3 µs and OFF-time of 0.34 µs at 1.0kHz. This is significantly faster than both Silicon Carbide (SiC) and Silicon (Si). Where, SiC MOSFET has ON- and OFF-times of 0.37 µs and 0.52 µs, respectively, while Si has ON- and OFF-times of 0.76 µs and 0.8 µs, respectively.

Fig. (15): Effect of MOSFET device materials on the switching transient times.

4.0. Conclusions

From the experimental work, results, analysis and interpretation, it could be concluded that**,** GaN power MOSFET (TPH3208PS-ND**)** emerges as a superior switching technology based on its electrical characteristics. This advantage stems from its lower ON-resistance and faster switching times. Additionally, SiC power MOSFET (TW107N65C**)** offers significant improvements over traditional Si power MOSFET (MTP20N15E).The timing performance of a switch is influenced by several factors inherent to the operating conditions and system design. These factors include load resistance, input voltage, and external gate resistance. For optimal switching circuit performance, a lower load resistance, lower external gate resistance, and higher input voltage are preferred.

5.0 Conflicts of Interest

The authors confirm that there are no known conflicts of interest associated with this publication and there is no relevant financial or non-financial interests to disclose.

References

- [1] E. O. Prado, et .al, "An Overview about Si, Superjunction, SiC and GaN Power MOSFET Technologies in Power Electronics Applications", Energies Journal, 15: (2022), 5244.
- [2] M. Beheshti, et .al, "Wide-bandgap semiconductors: Performance and benefits of GaN versus SiC", Analog Design Journal, (2020).
- [3] S. K. Pullabhatla, et .al, "Comparison of GAN, SIC, SI Technology for High Frequency and High Efficiency Inverters",Vol. 184, 2020 Web of Conferences Journal.
- [4] L. Lorenz, et al., "Future technology trends", Wide Bandgap Power Semiconductor Packaging,Woodhead Publishing, (2018), pp. 3-53, ISBN 9780081020944.
- [5] S. Bufan, et. al., "A review of silicon carbide MOSFETs in electrified vehicles: Application, challenges, and future development", IET Power Electron. Journal,16: (2023), 2103–2120.
- [6] A. Lidow, et. al., "The semiconductor roadmap for power management in the new millennium", in Proceedings of the IEEE, 89: (2001), 803-812.
- [7] T. Ytterdal, et al., 2003 in John Wiley & Sons, Ltd"Device Modeling for Analog and RF CMOS Circuit Design", ch.1.
- [8] P.E. Jonathan Dodge, "Power MOSFET Tutorial'', Application Note, APT-0403 Rev B, Advanced Power Technology, 2006.
- [9] A. Sattar, "IXAN0061 Power MOSFET Basics'', Application Note ,IXYS Corporation, Milpitas, CA (2010).
- [10] G. Rajendran, et. al., "Hard Switching Characteristics of SiC and GaN Devices for Future Electric Vehicle Charging Stations", 2021 MATEC Web of Conferences, Vol. 335, 2007.
- [11] Y. Zhong, et. al., "A review on the GaN-on-Si power electronic devices", Fundamental Research, 2:(2022) 462-475, ISSN 2667-3258.
- [12] S. Manias, "Power Electronics and Motor Drive Systems",Academic Press, (2017) pp. 695- 805.
- [13] Vishay, "Power MOSFET Basics: Understanding Gate Charge and Using it to Assess Switching Performance", Device application note, (2016).
- [14] M. H. Rashid, 2011 in Butterworth-Heinemann (3rd. Ed.) Power Electronics Handbook" pp.83-93, ISBN 9780123820365.
- [15] F. J. Hsu, et. al., "A Dynamic Switching Response Improved SPICE Model for SiC MOSFET with Non-linear Parasitic Capacitance", IEEE Workshop on Wide Bandgap Power Devices and Applications in (WiPDA Asia), Suita, Japan, (2020) pp. 1-4. https://doi: 10.1109/WiPDAAsia49671.2020.9360267.
- [16] T. Aichinger, et. al., "Assessing, Controlling and Understanding Parameter Variations of SiC Power MOSFETs in Switching Operation", Key Engineering Materials, 947: (2023) pp. 69- 75.
- [17] P. Salmen, et. al., "Gate-switching-stress test: Electrical parameter stability of SiC MOSFETs in switching operation", Microelectronics Reliability, 135: (2022), 114575, ISSN 0026- 2714. https://doi.org/10.1016/j.microrel.2022.114575.
- [18] N. Carley, "Timing of Load Switches", Application report, Texas instruments, 2017.

الملخص العربى

تأثير نوع مواد ترانزستور المجال األكسيدي المعدني ذات القدرة

على أدائها كمفاتيح الكترونية

أسماء سلامة عبدالتواب إبراهيم (')ـ ثناء عبدالتواب قمح (')ـ فؤاد عبد المنعم سعد سليمان'') ـ دعاء حسن حنفي **),1*(محمود**

'معمل أبحاث الإلكترونيات- قسم الفيزياء- كلية البنات للأداب والعلوم والتربية- جامعة عين شمس- القاهرة- جمهورية مصر العربية. 2 قسم هندسة اإللكترونيات، هيئة المواد النووية، وزارة الكهرباء والطاقة المتجددة- القاهرة- جمهورية مصر العربية.

المخلص العربي :

تناول البحث المعروض دراسة عملية وتطبيقية عن جدوى إستخدام نبائط أشباه الموصالت ذات القدرة)MOSFET) في بعض تطبيقات الأنظمة الإلكترونية التي تخدم المجتمع الحديث، حيث تم تطويرتلك النبائط باستخدام مواد ذات نطاق طاقي مختلفة لتحسين أداء عملها كمفاتيح إلكترونية. في هذا الصدد، تم إختيار ودراسة الخصائص الكهربائية المميزة لثالثة نبائط SiC-TW107N65C; Eg = (السيليكون وكربيد(Si-MTP20N15E; Eg = 1.12 eV(السيليكون من مصنعة مختلفة eV 3.25)ونيتريد الجاليوم) eV 3.44 = Eg; ND-PS3208TPH -GaN). حيث تم إستنتاج المعامالت الكهربائية المختلفة بهدف تصميم و تنفيذ دوائر المفاتيح اإللكترونية، تعمل عند تردد 1.0 كيلو هرتز، بنمط تشغيل المعزز) Enhancment mode). حيث أثبتت الدراسة أن نبيطة الترانزستور من مادة نيتريد الجاليوم سجلت أسرع إستجابة بأقل زمن تشغيل / إيقاف (µs /0.34 µs0.32(، عن مادتي كربيد السيليكون و السيليكون. حيث سجلت أزمنة تشغيل / إيقاف الخاصة بهم (µs/0.8 µs0.76 and µs/0.52µs0.37(، على الترتيب. حيث أكدت هذه النتائج على إمكانية المواد ذات النطاق الطاقي الواسع في تعزيز أداء نبائط أشباه الموصالت ذات القدرة)MOSFET).